



SRC Computers, Inc.
4240 N Nevada Ave
Colorado Springs, CO 80907
(719) 262-0213
www.srccomputers.com
E-mail: marketing@srccomputers.com

SRC Announces Support for AMD Torrenza Initiative Testing Underway with HP Servers

COLORADO SPRINGS, Colo. – June 18, 2007 – SRC Computers, Inc., a leader in reconfigurable computing systems, today announced its support for AMD's Torrenza initiative and will incorporate its heterogeneous IMPLICIT+EXPLICIT architecture and Carte programming environment into servers with AMD microprocessors. This will enable AMD Opteron™ processor-based servers to deliver orders of magnitude more performance while consuming orders of magnitude less power in a smaller footprint. The accelerated general purpose servers will be used across a broad range of applications using standard high-level languages and programming techniques.

According to Jon Huppenthal, President and CEO of SRC, "The effort that is underway incorporates SRC's reconfigurable MAP processors, high-bandwidth SNAP interconnect and high-level language programming environment into AMD platforms. As a result, applications developed for SRC systems will be software compatible regardless of the microprocessor type."

"The addition of SRC to the Torrenza initiative is welcomed. Developers will now be able to use ANSI standard languages to develop applications that use both SRC's reconfigurable hardware and the AMD Opteron™ microprocessor. We see this combination as very powerful in many markets including financial services, oil and gas exploration, and life sciences" says Randy Allen, Vice President Server and Workstation Division of AMD.

While SRC ultimately expects to support servers from several manufacturers, initial testing is underway using HP ProLiant DL385 servers.

"The rapidly increasing demand for computational performance in HPC is leading many customers to consider heterogeneous processing solutions," said Winston Prather, vice president and general manager, High Performance Computing, HP. "We're pleased that SRC has chosen HP servers for their testing."

SRC is the only reconfigurable system vendor that also provides a tightly integrated ANSI standard C and Fortran high-level language programming environment with both development and debug capability. SRC expects to have AMD-based systems available in the fourth quarter of 2007.

About SRC

SRC Computers, Inc. is a privately owned company established in 1996 in Colorado Springs, Colorado by legendary computer architect Seymour Cray. SRC has developed the IMPLICIT+EXPLICIT architecture that provides orders of magnitude increases in performance over conventional microprocessors. Because this SRC-developed software and hardware architecture is applicable to products ranging from uniprocessor handheld devices to large-scale multiprocessor computer systems, SRC is able to offer solutions targeted at the high performance server and workstation markets as well as the embedded markets. In addition to its headquarters in Colorado Springs, SRC also maintains a software development facility in Minneapolis, Minnesota. Carte, Compact MAP, Hi-Bar, SNAP, MAPstation, IMPLICIT+EXPLICIT, and MAP are trademarks or registered trademarks of SRC Computers, Inc. All other trademarks and registered trademarks are the property of their respective owners. SRC's website address is www.srccomputers.com.

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Media Contact – Valerie Jackson, Marketing Communications, (719) 785-5119, vjackson@srccomputers.com

Technical Contact – Jon Huppenthal, President & CEO, (719) 262-0213, hupp@srccomputers.com